PAIOL Project

As initially submitted to the ALFA programme:

- To build a parallel Logic Programming system employing techniques already proved to be efficient in other Logic Programming systems (Andorra-I, MultiPath, Aurora, Muse, ACE) and provide support to constraints.
- To study alternatives to the traditional Logic Programming systems.
- To study the performance of different machine architectures through several applications.
- To write real Logic Programming applications.
- To design and study a new general purpose computer architecture, learning from the study of performance of the existing architectures.